



Sir:

Docket: 0756-1398 Date: August 25, 1995

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Honorable Commissioner of Patents and Trademarks Washington, D.C. 20231

Transmit	ted herewith for filing is the patent application of:
Inventors	: Takeshi FUKUNAGA, Hisashi OHTANI, Akiharu MIYANAGA
For: A M	ETHOD OF MANUFACTURING A SEMICONDUCTOR DEVICE
Enclosed	are:
[X] 50	pages of specification, claims and abstract
	O sheets of formal drawings (Figures 1(ABCD), 2(ABC), 3(ABCDE), (ABCDEF), 5(ABCD), 6(ABCDEF), 7, 8, 9(ABCD), and 10)
	n assignment of the invention to <u>Semiconductor Energy Laboratory</u> o., <u>Ltd.</u> .
[X] D	eclaration and Power of Attorney.
N Fi	riority is claimed based on foreign application(s) o(s): 6-225851 iled on: August 26, 1994 iled in: Japan ertified copy(s) [X] attached
	verified statement to establish small entity status under 37 CFR 1.9 and 37 CFR 1.27.
[] O	ther:

[X] The filing fee has been calculated as shown below.

For:	No. Filed		No. Extra	Rate Sm/Lg Entity	Fee
Basic Fee				\$365/730	\$730.00
Total Claims	22	- 20	2	x 11/22	\$ 44.00
Independent Claims	7	- 3	4	x 38/76	\$304.00
First presenta					
Assignment R	\$ 40.00				
Total	\$1,118.00				

- [X] A check in the amount of \$1,118.00 to cover the filing fee is enclosed.
- [X] The Commissioner is hereby authorized to charge fees under 37 CFR 1.16, 1.17, 1.20(a), 1.20(b), 1.20(c) and 1.20(d) (except the Issue Fee) which may be required now or hereafter, or credit any overpayment, to Deposit Account No. 19-2380. A duplicate copy of this sheet is attached.

Respectfully submitted,

Gerald J. Ferguson, Jr.

Registration No. 23,016

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